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APPLICATION NO.	FILING DATE	FIRADEMA FIR	RST NAMED INVEN	ΓOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/081,109	02/25/2002		Shinsuke Sakamot	0	4329.2270-01	4805
TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND WIRING ARRANGING METHOD THEREOF						
APPLN. TYPE	SMALL ENTITY	ISSUE FEE	E PUBLICATION FEE		TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330		\$300	\$1630	06/29/2004
EXAMINER		ART UNIT	CL	ASS-SUBCLASS	j	
LEE, EUGENE		2815		257-776000		
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Richard V. Burguijan, Reg. No. 31,744				06/30/2004 St	11NASS2 00000076 100811	109
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